



MAY 27 2003
Attorney Docket No. 5649-952

2818
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Jae-goo Lee
Serial No.: 10/068,148
Filed: February 6, 2002
For: INTEGRATED CIRCUIT MEMORY DEVICES WITH PER-BIT
REDUNDANCY AND METHODS OF OPERATING SAME

Group Art Unit: 2818
Examiner: Son Luu Mai
Conf. No.: 7507

DATE: May 21, 2003

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO MAY 15, 2003 OFFICE ACTION

Sir:

This is responsive to the Office Action of May 15, 2003 ("Office Action"), which indicates our response to the February 6, 2003 Office Action was not fully responsive. This response is provided in the Revised Amendment Format as published in the Official Gazette on February 25, 2003. The Revised Amendment Format waives certain requirements of 37 CFR 1.121.

It is not believed that an extension of time and/or additional fee(s), including fees for additional claims, are required, beyond those that may otherwise be provided for in documents accompanying this paper. In the event, however, that an extension of time is necessary to allow consideration of this paper, such an extension is hereby petitioned under 37 C.F.R. §1.136(a). Any additional fees believed to be due in connection with this paper may be charged to our Deposit Account No. 50-0220.

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